

FIG. 1a
(Prior Art)

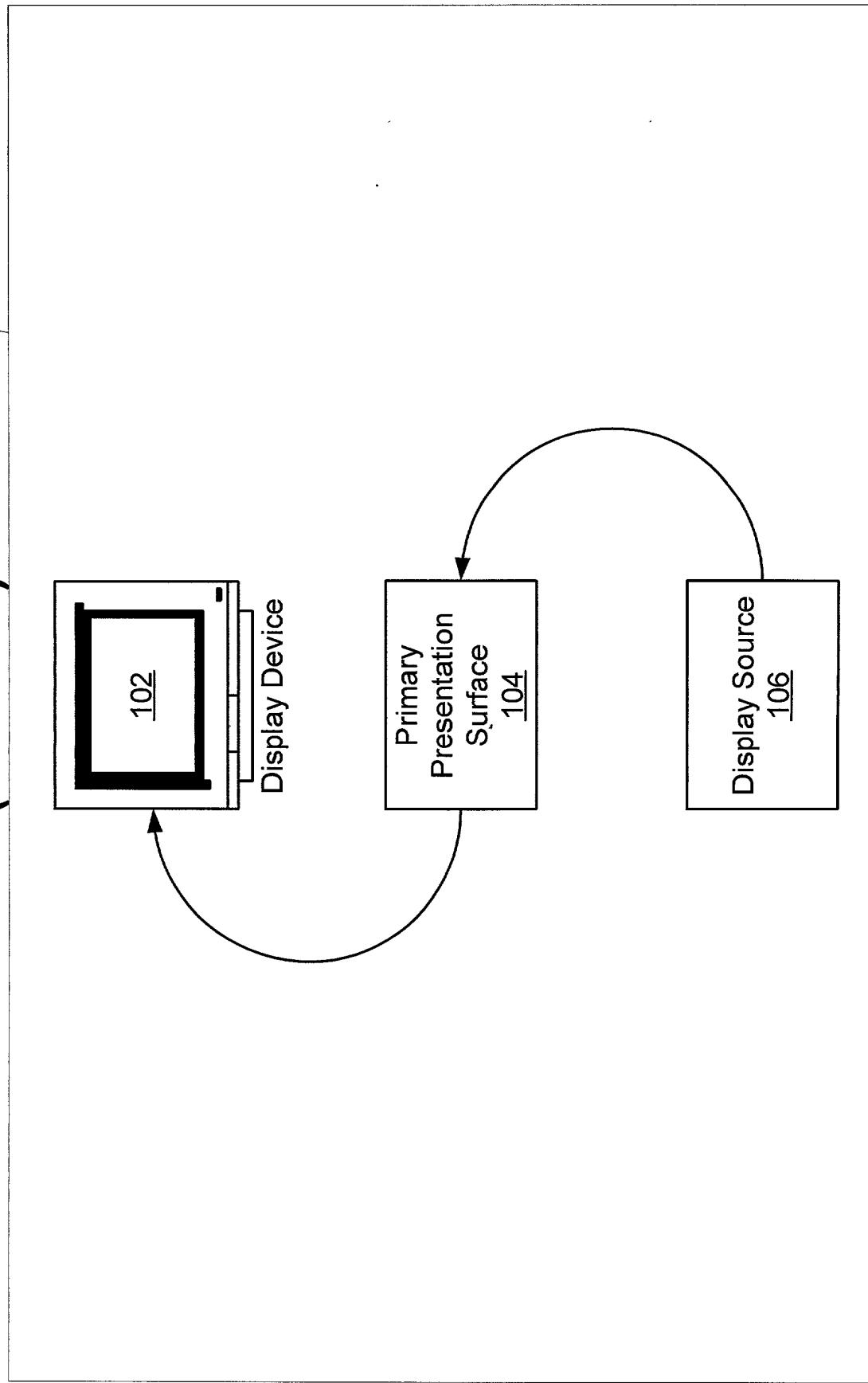


FIG. 1b (Prior Art)

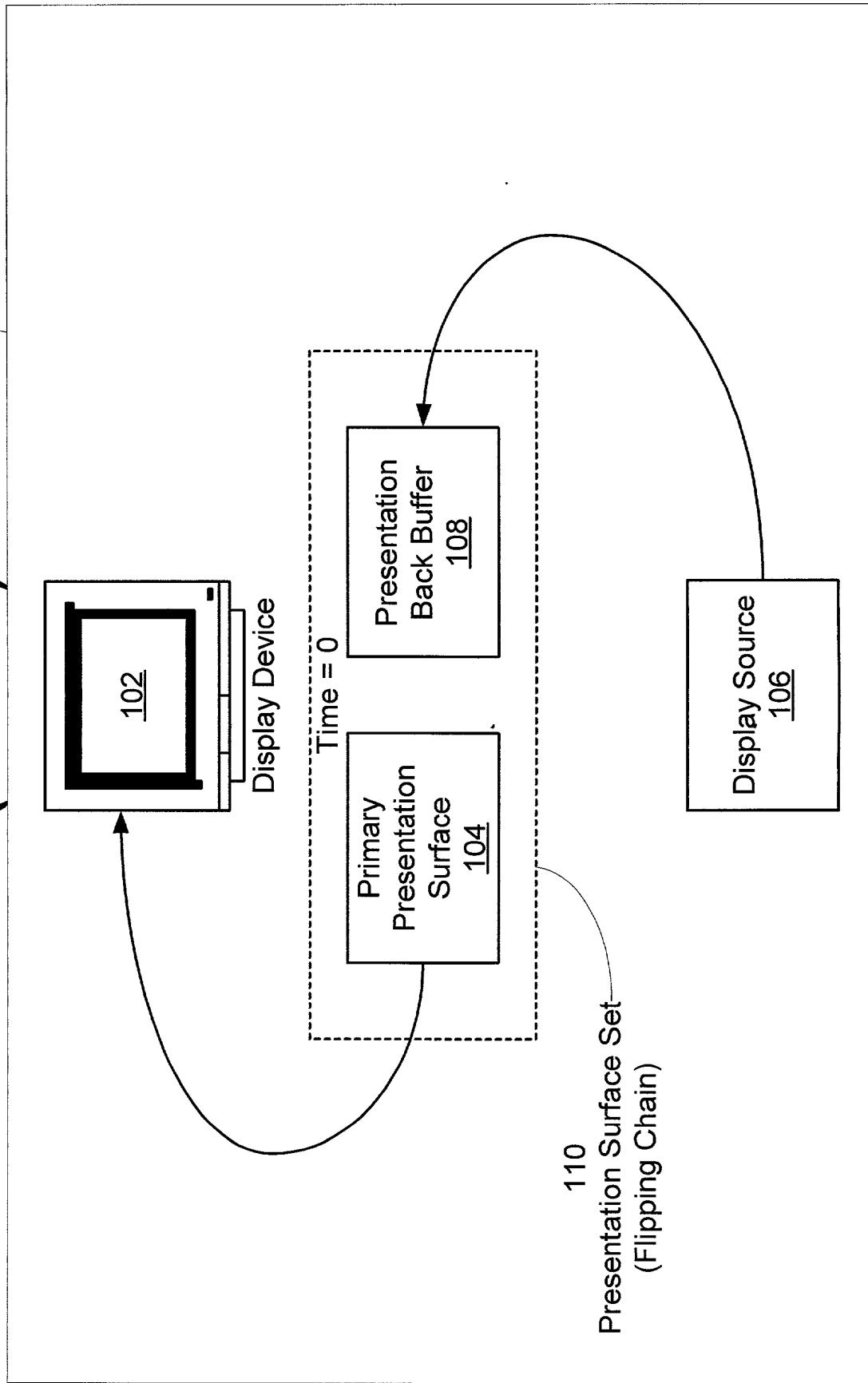


FIG. 1c
(Prior Art)

100

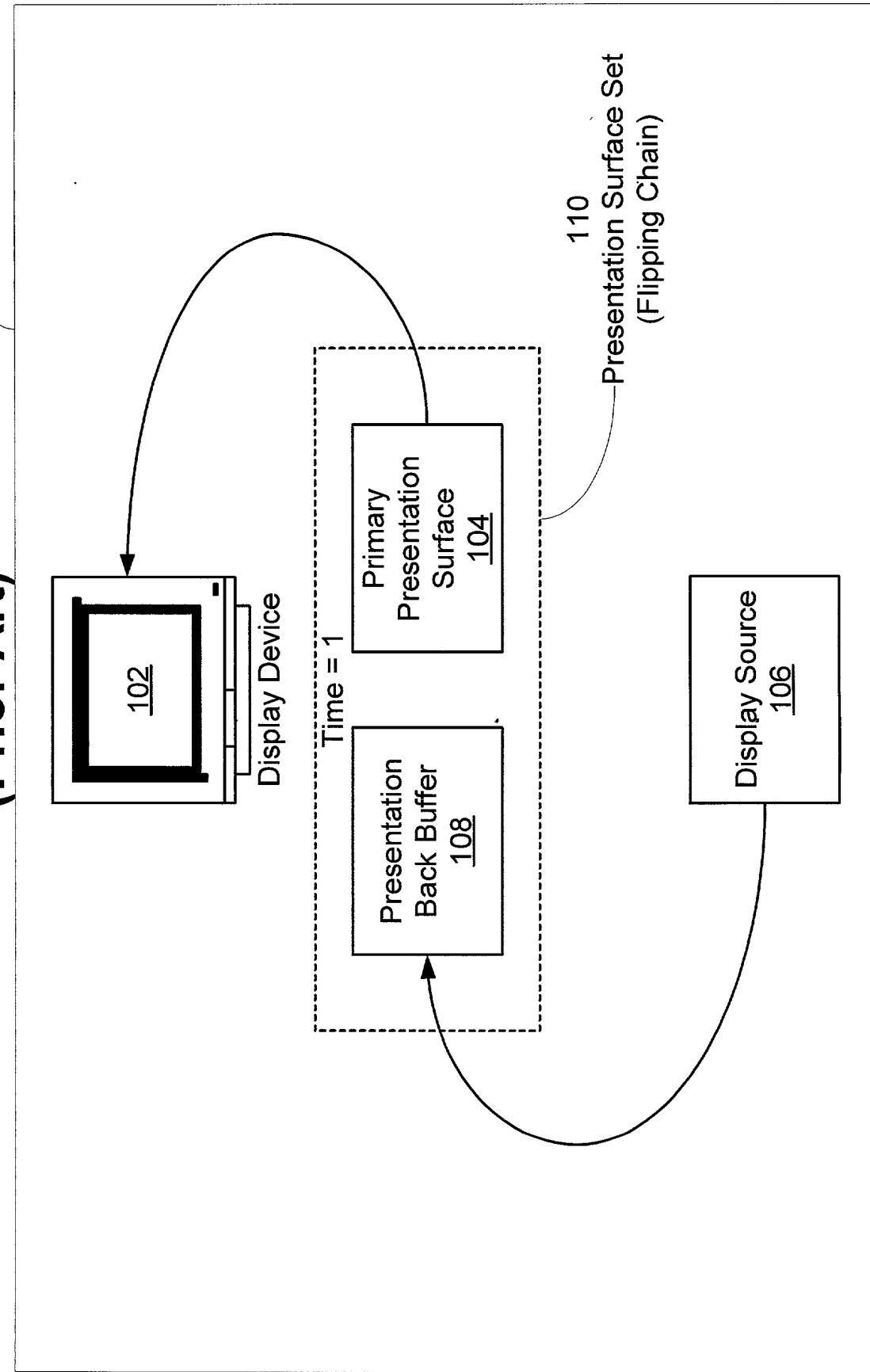
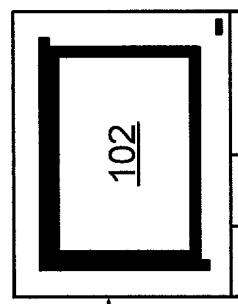


FIG. 1d (Prior Art)

100

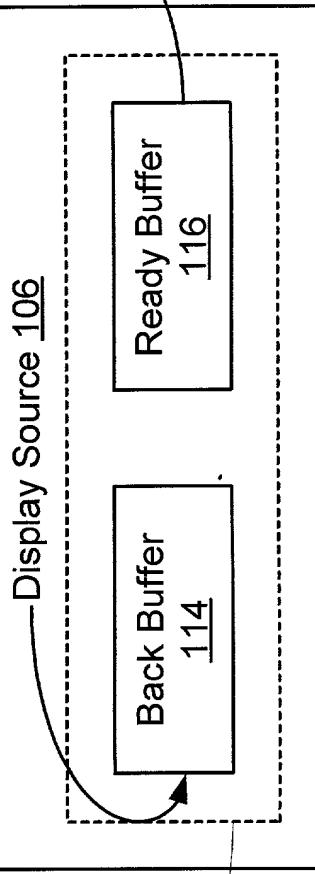


Display Device

110
Presentation Surface Set
(Flipping Chain)

Presentation
Back Buffer
108

Primary
Presentation
Surface
104



112
Memory Surface Set
(Flipping Chain)

Ready Buffer
116

Back Buffer
114

FIG. 1e (Prior Art)

100

110
Presentation Surface Set
(Flipping Chain)

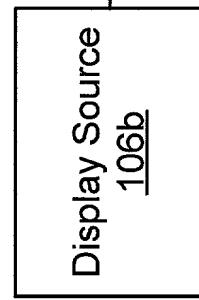
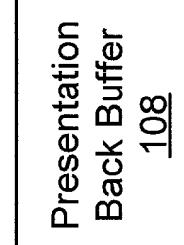
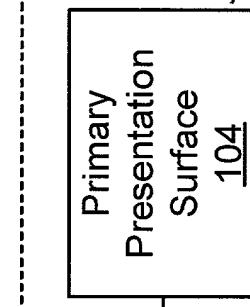
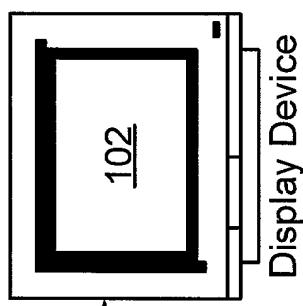


FIG. 2a (Prior Art)

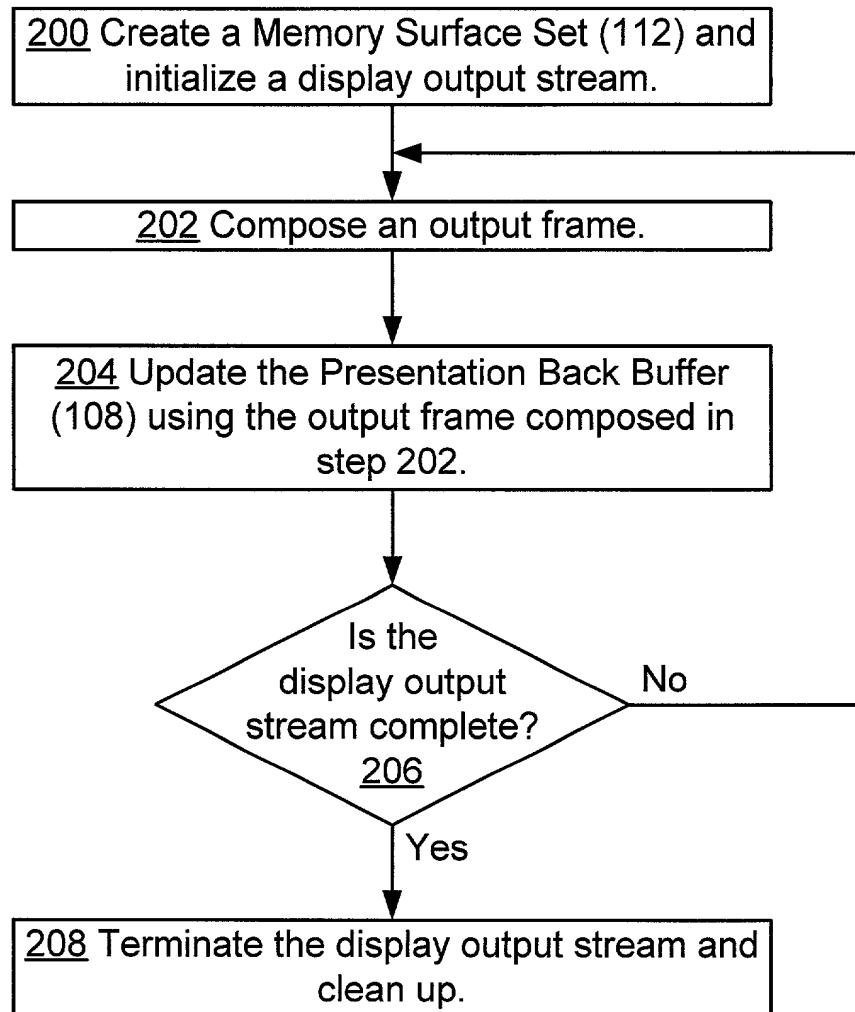


FIG. 2b (Prior Art)

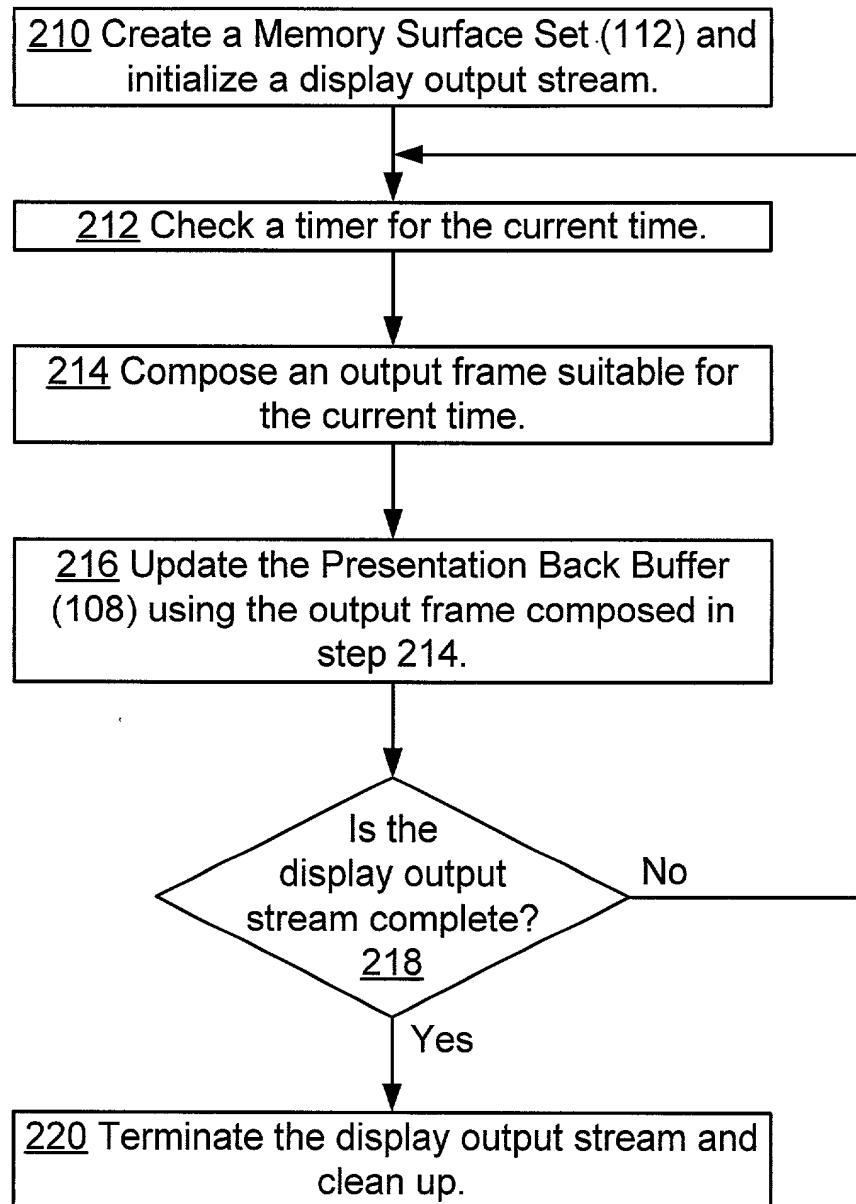


FIG. 2c (Prior Art)

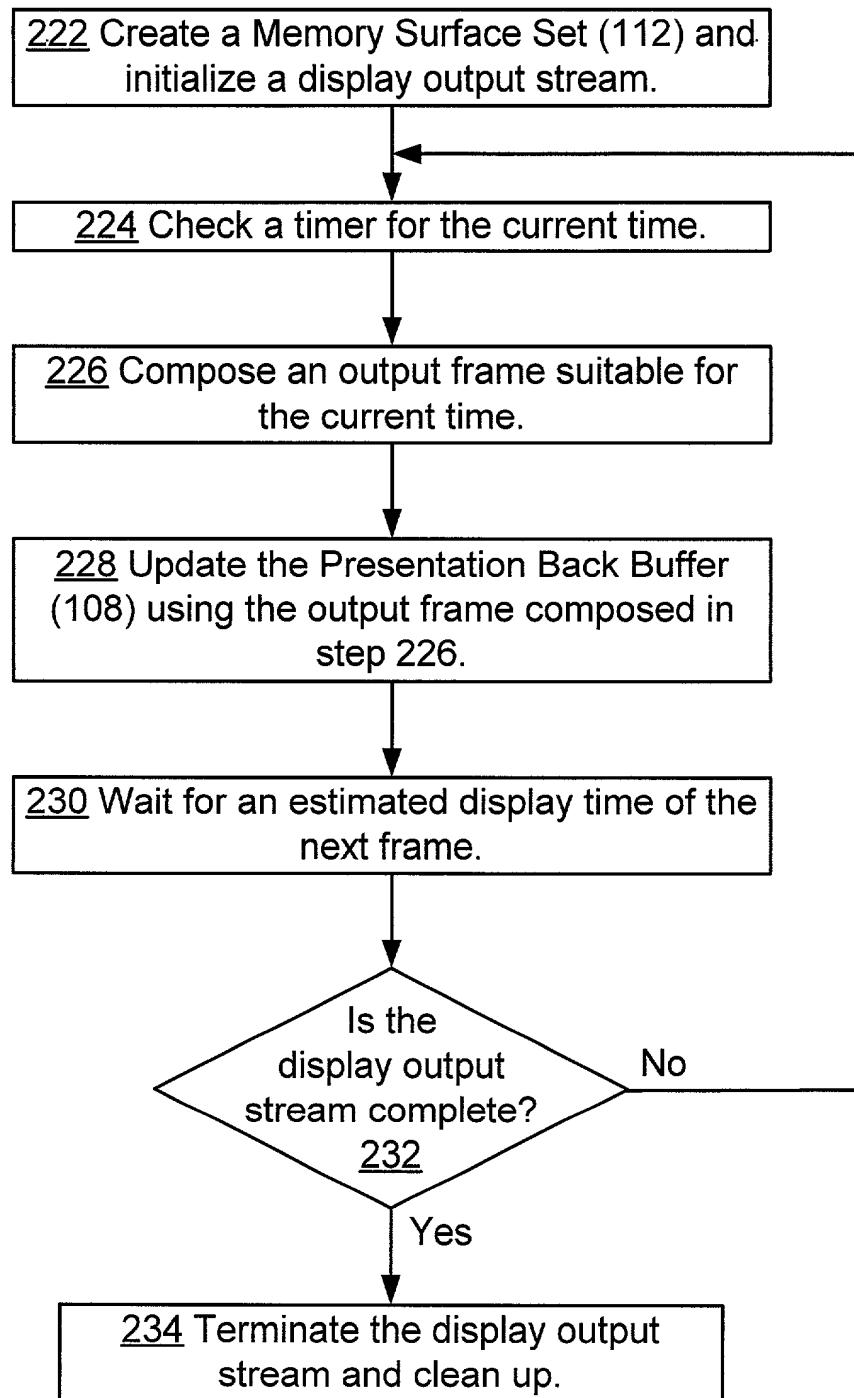


FIG. 3

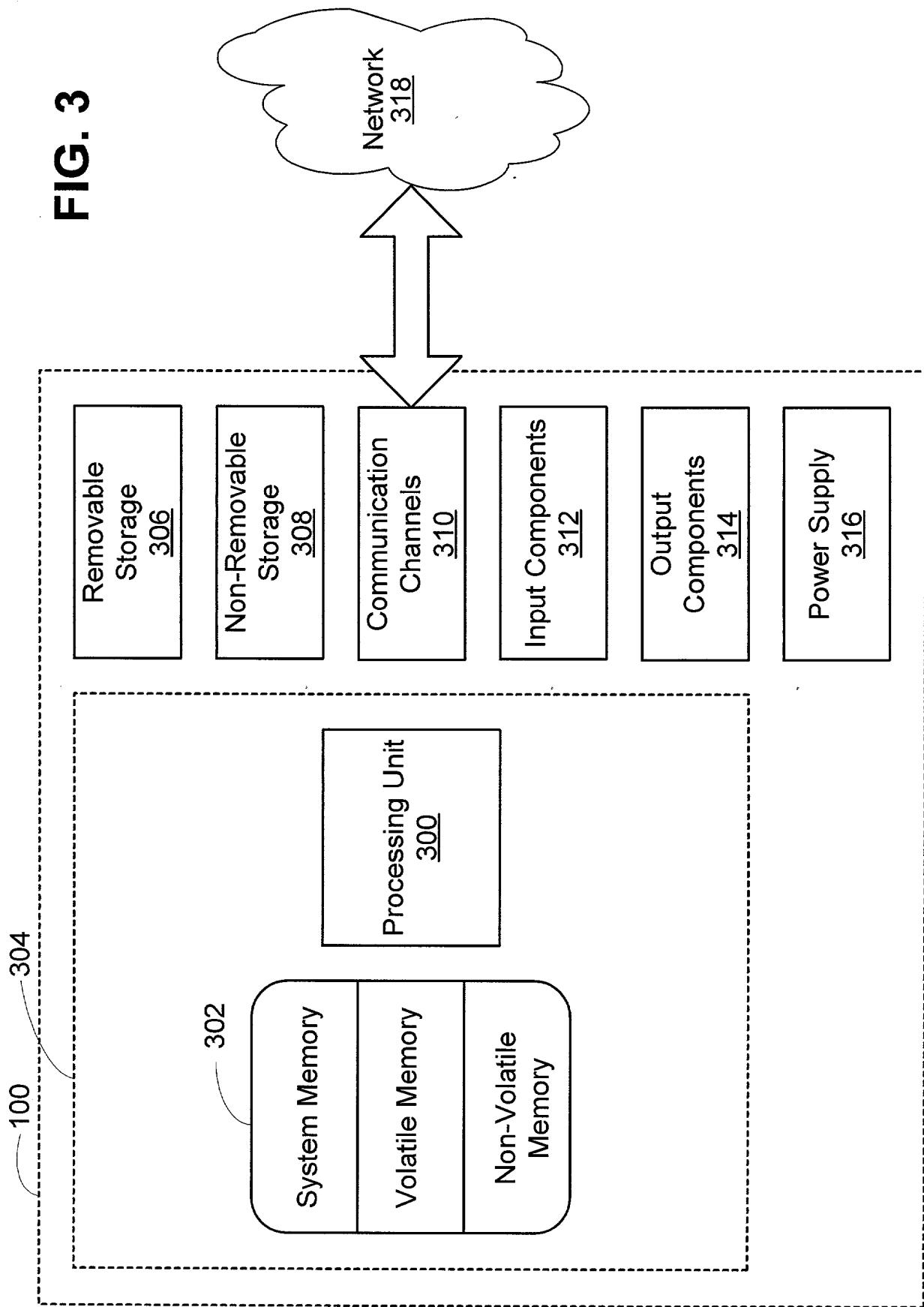


FIG. 4 100

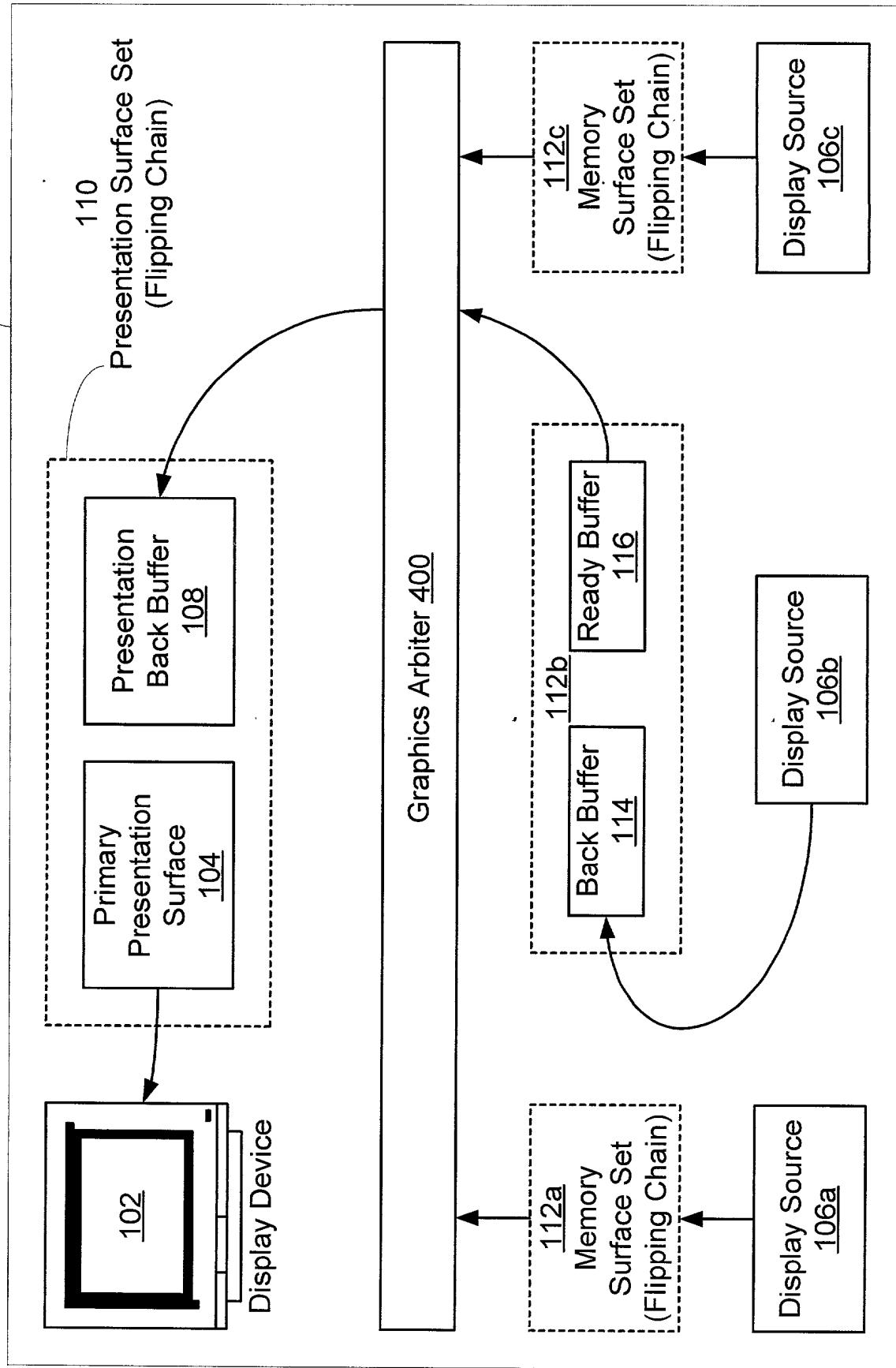


FIG. 5

100

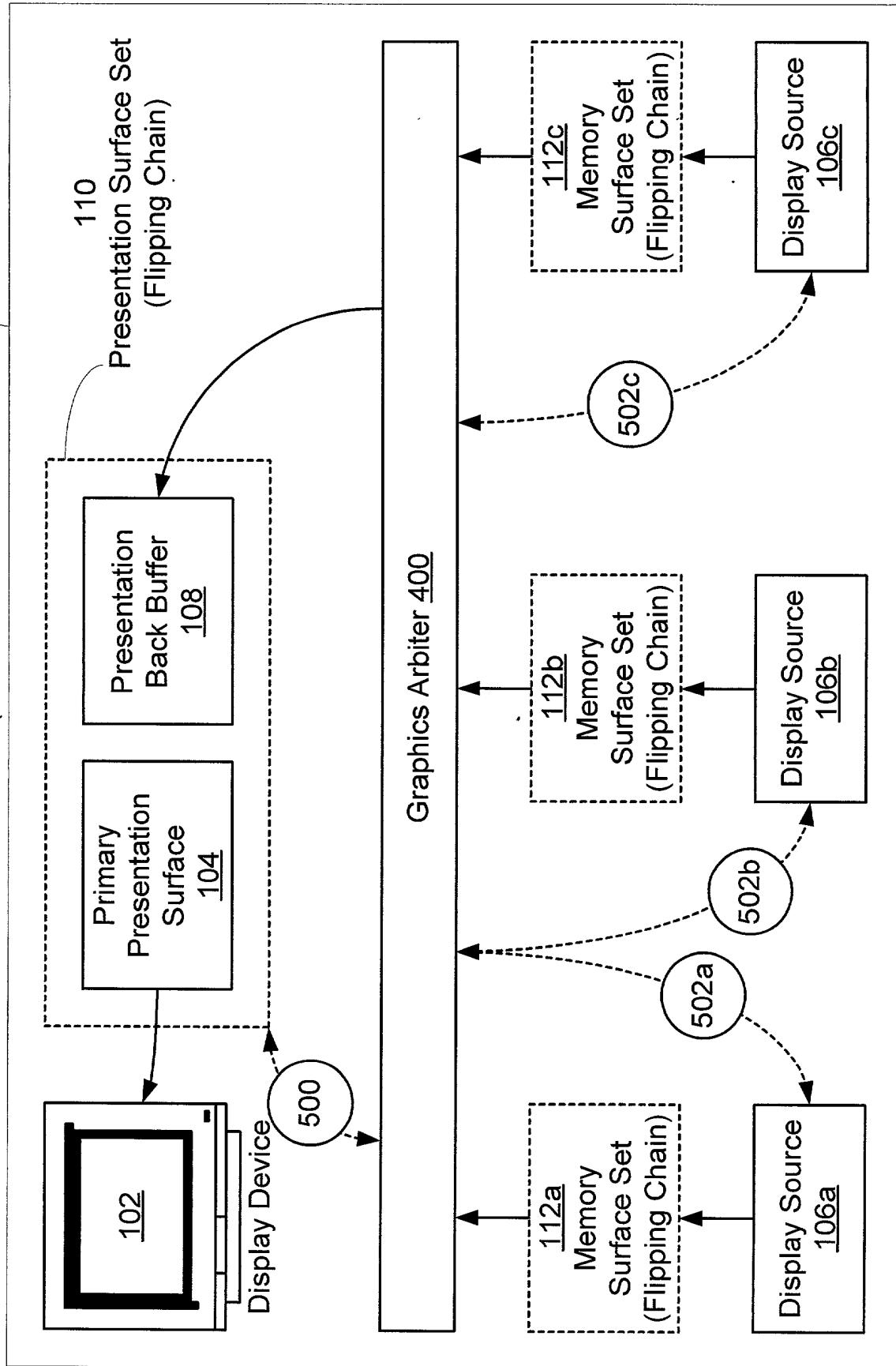


FIG. 6

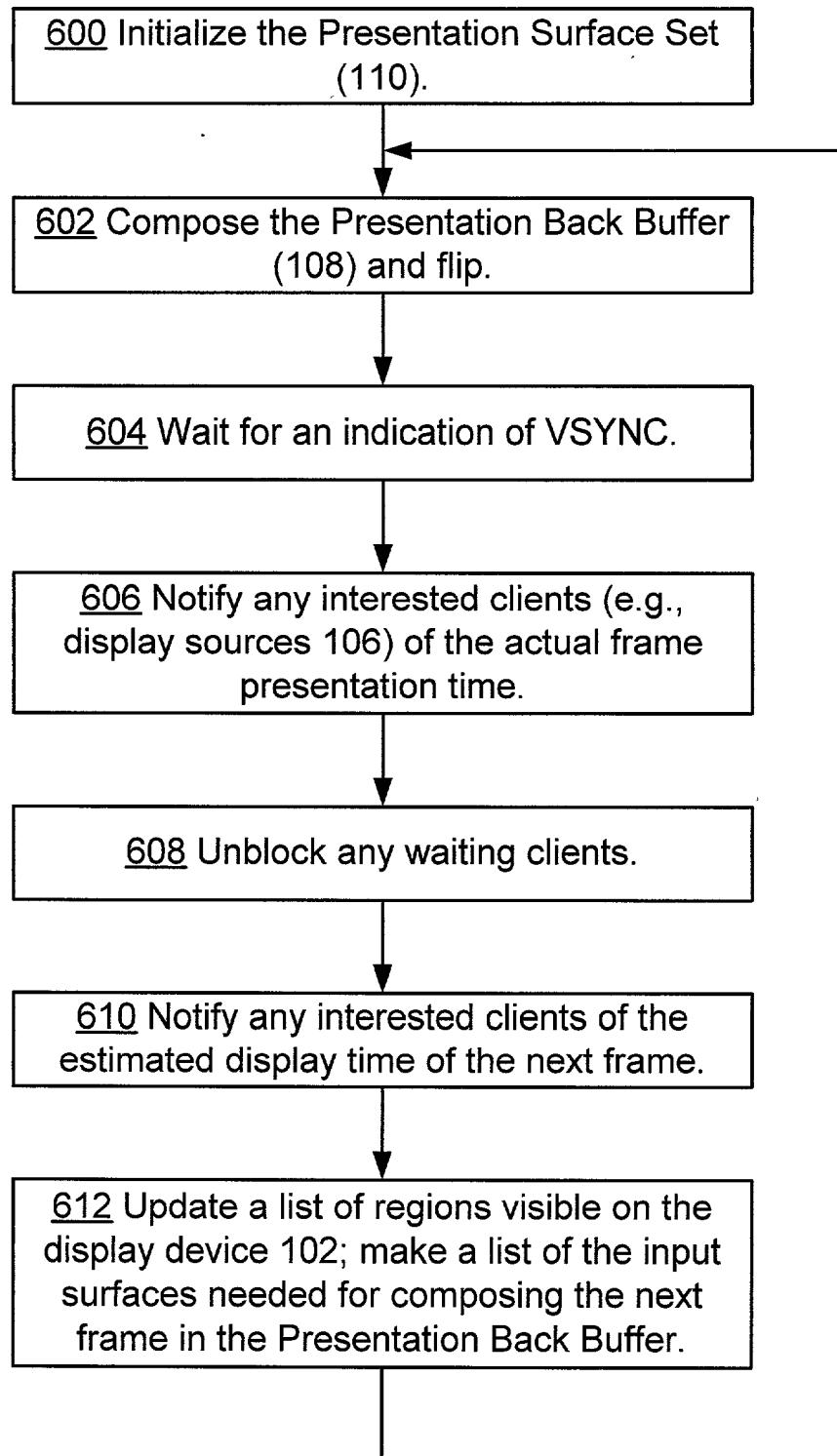


FIG. 7a

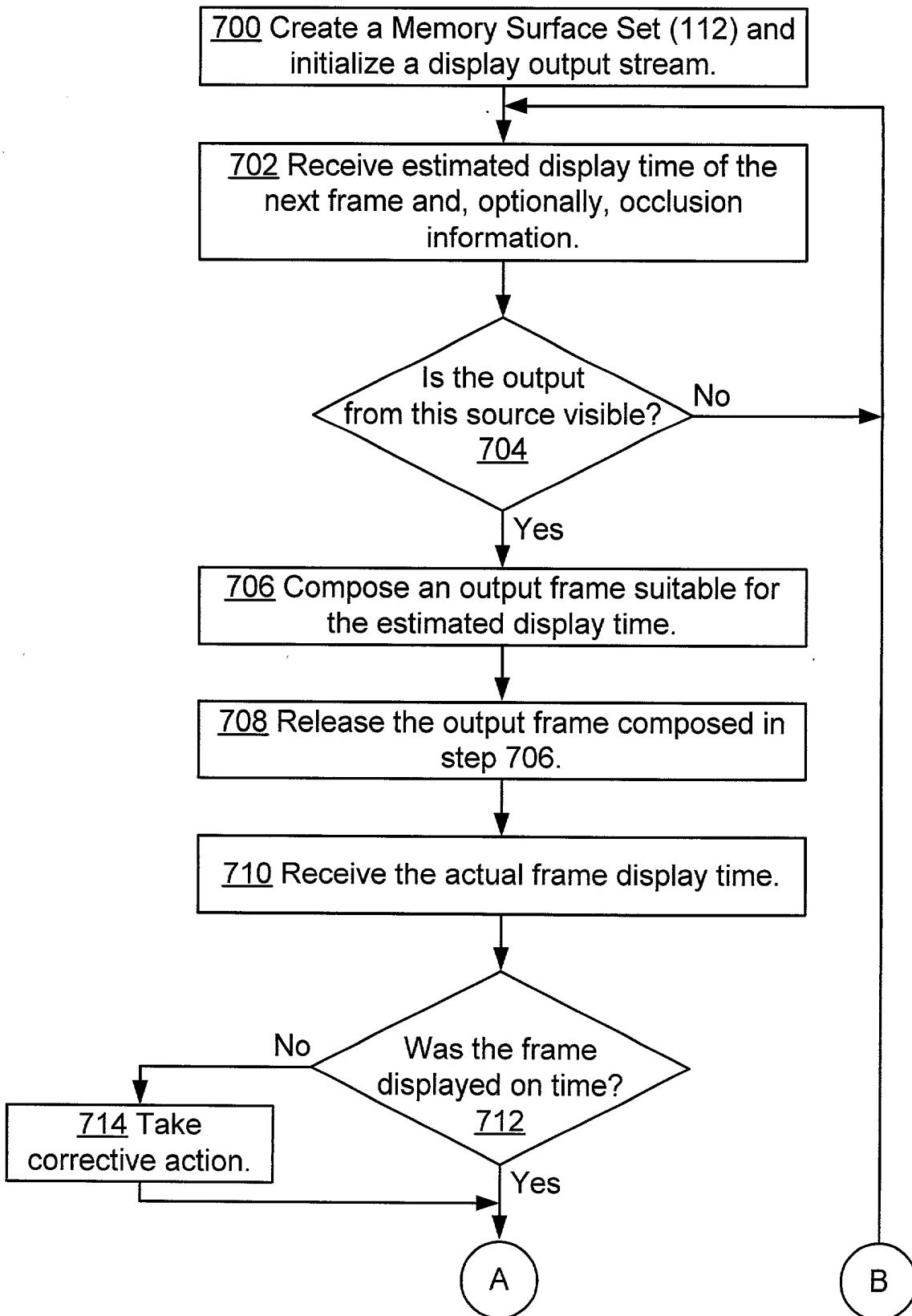


FIG. 7b

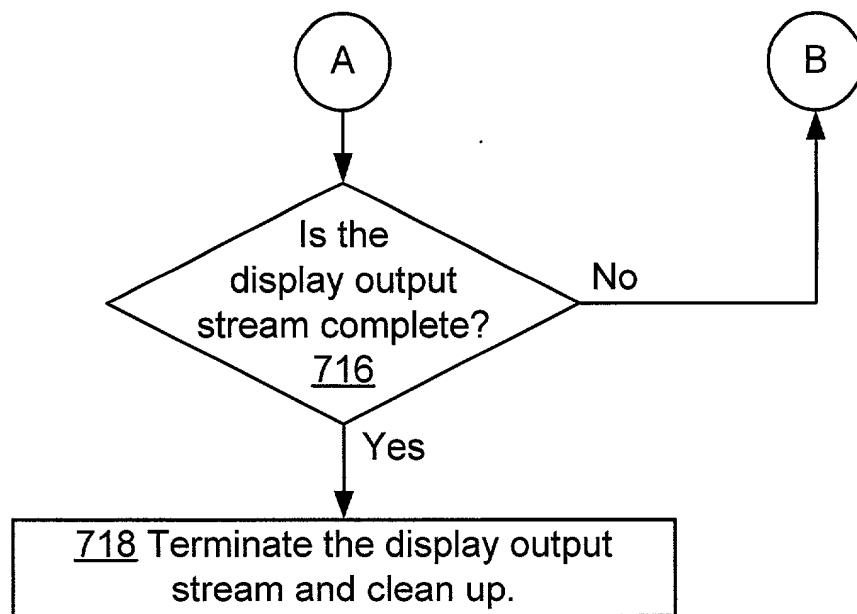


FIG. 8

100

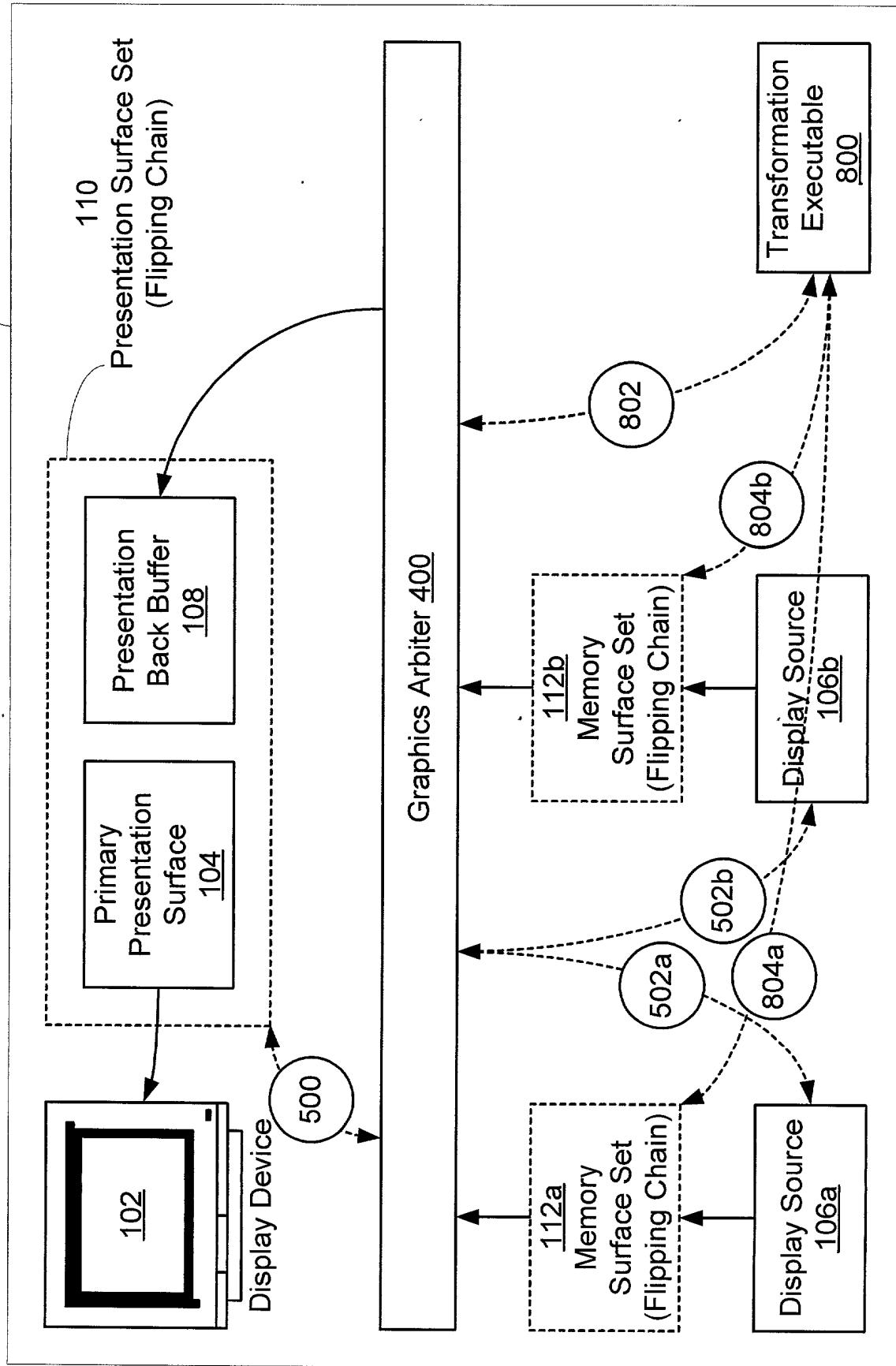


FIG. 9

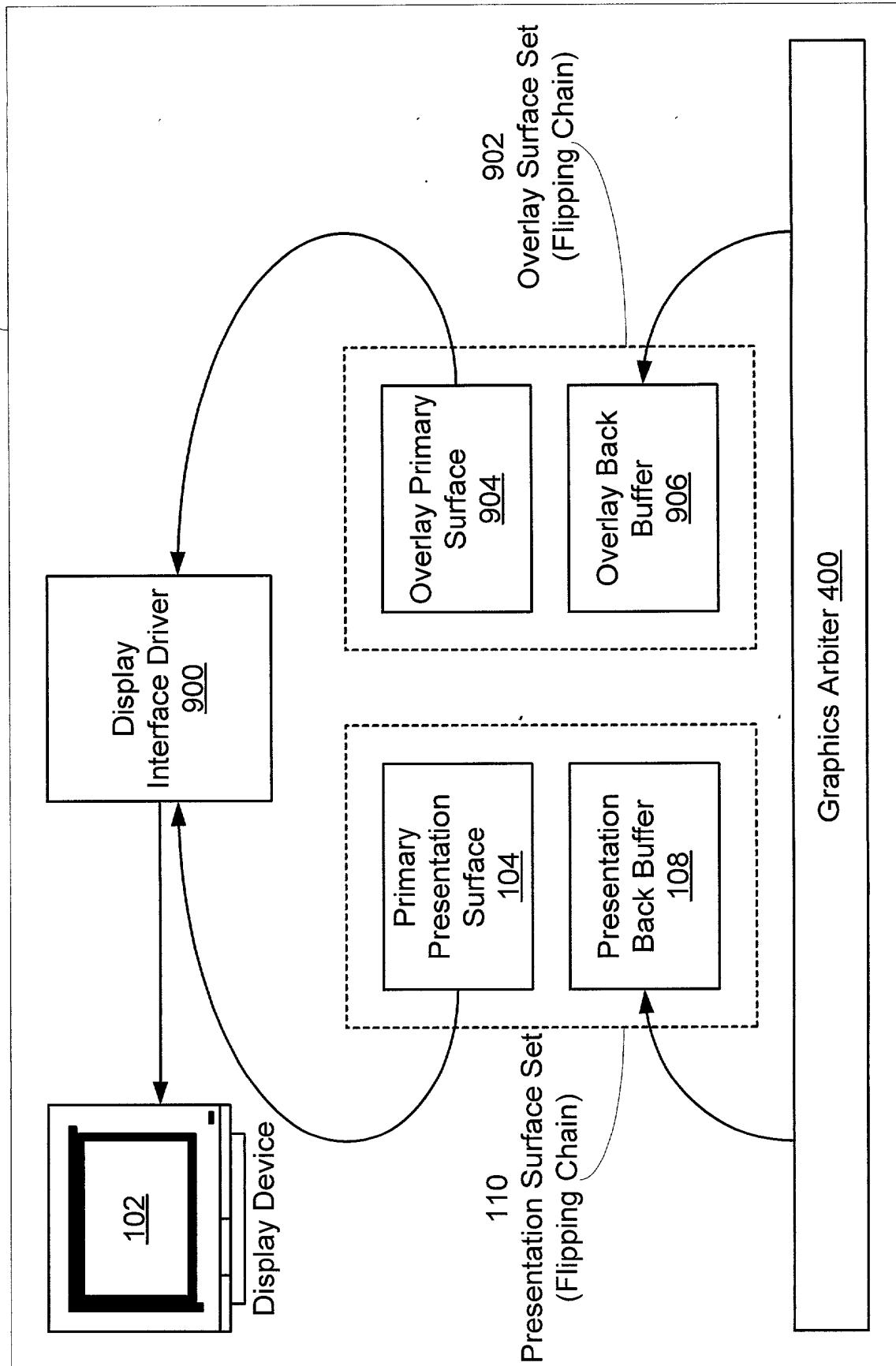


FIG. 10

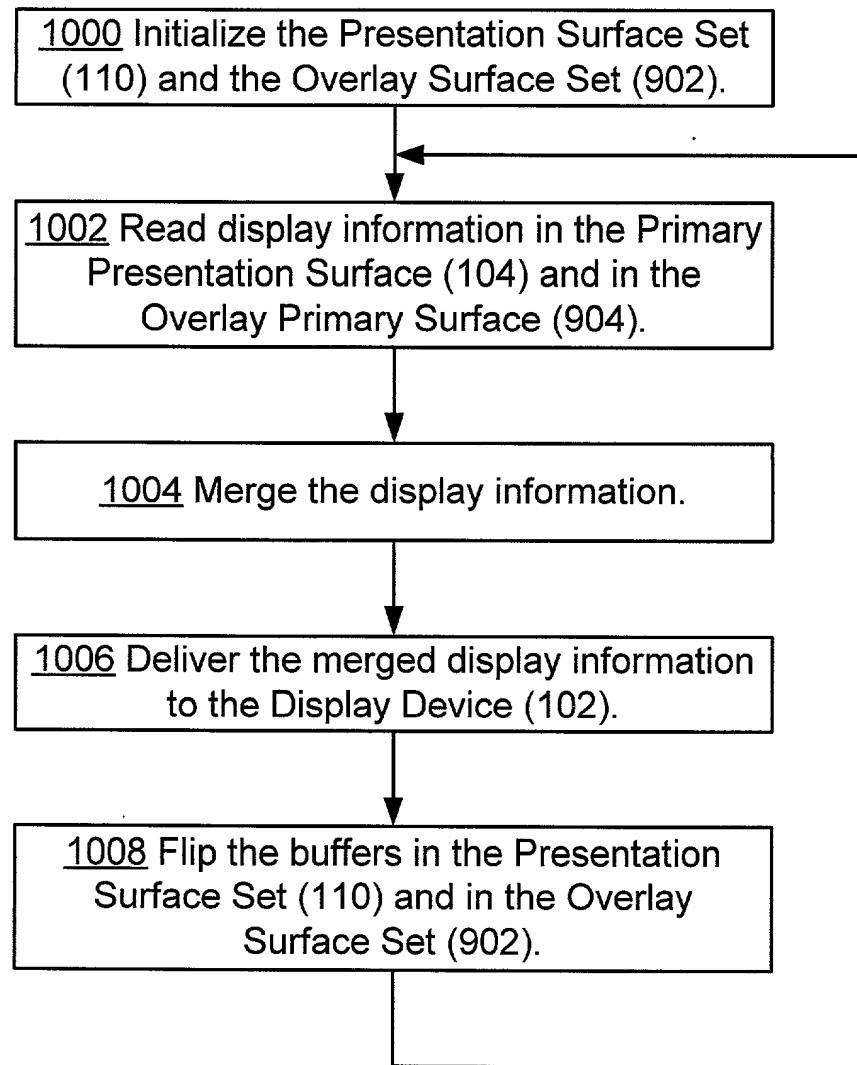


FIG. 11

